

FIG. 3

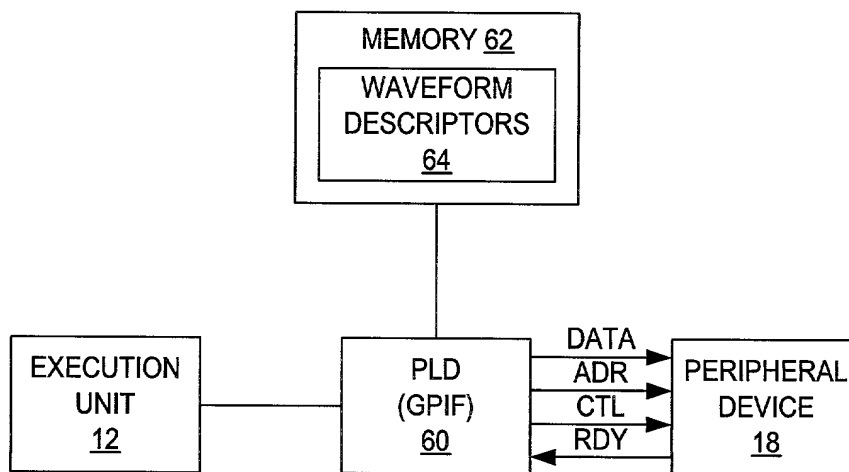


FIG. 4

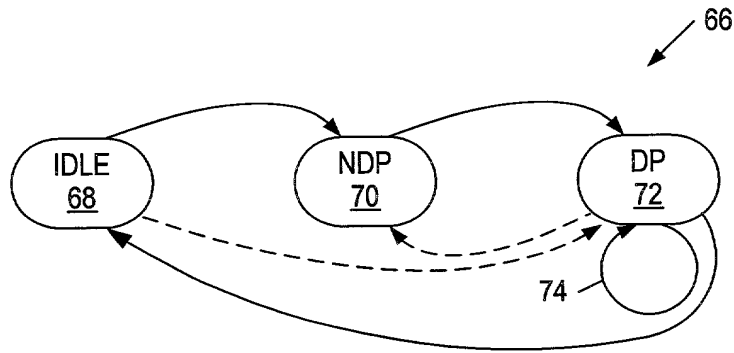


FIG. 5

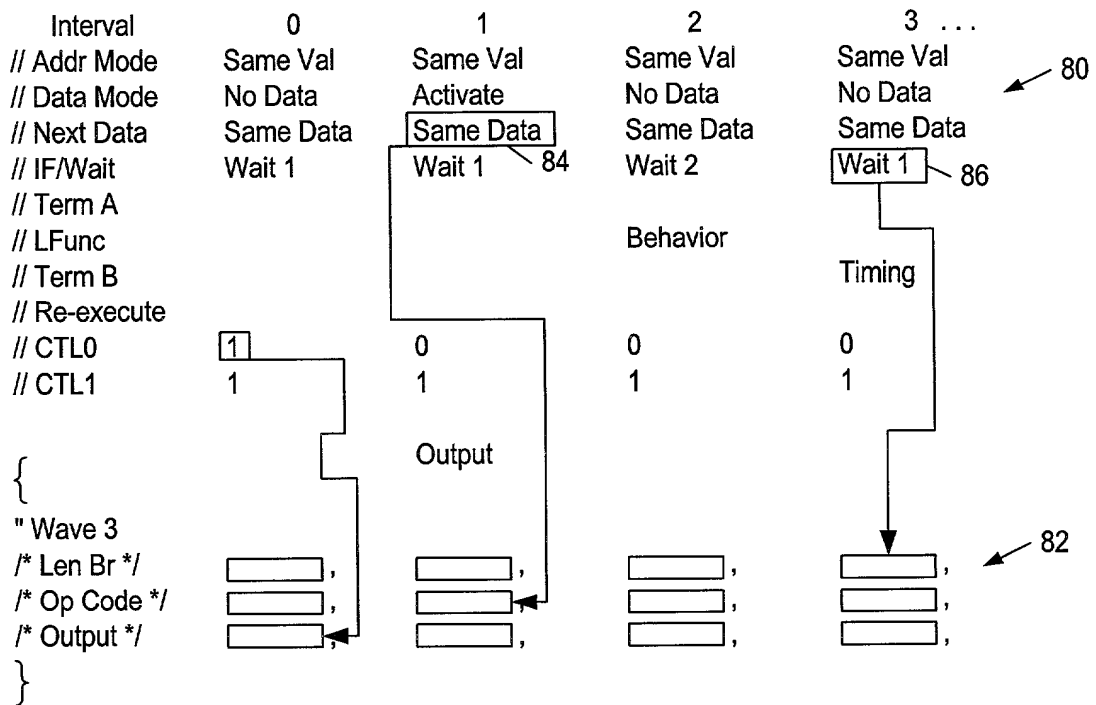


FIG. 6

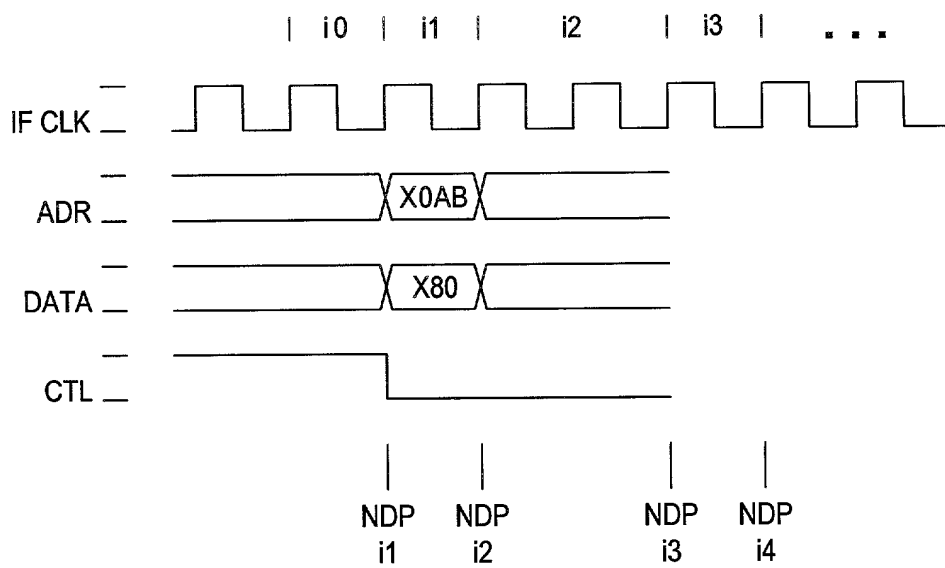


FIG. 7

```

#define PERIPHCS 0x00AB
#define AOKAY 0x80
#define BURSTMODE 0x0000
#define TRISTATE 0xFFFF
#define EVER ;;

// prototype
void GPIFInit ( void );

// Set Address GPIFADR [8:0] to PERIPHERAL
void Peripheral_SetAddress ( WORD gaddr )
{
    if ( gaddr < 512 )
    { // drive GPIF address bus w/gaddr
        GPIFADRH = gaddr >> 8;
        GPIFADRL = ( BYTE ) gaddr; // setup GPIF address
    }
    else
    { // tri-state GPIFADR [8:0] pins
        PORTCCFG = 0x00; // [7:0] as port I/O
        OEC = 0x00; // and as inputs
        PORTECFG &= 0x7F; // [8] as port I/O
        OEC &= 0x7F; // and as input
    }
}

// read single byte from PERIPHERAL, using GPIF
void Peripheral_SingleByteRead ( BYTE xdata *gdata )
{
    static BYTE g_data = 0x00

    while( ! ( GPIFTRIG & 0x80 ) ) // poll GPIFTRIG.7 Done bit
    {
        ;
    }

    // using register(s) in XDATA space, dummy read
    g_data = XGPIFSGLDATLX; // to trigger GPIF single byte read transaction

    while( ! ( GPIFTRIG & 0x80 ) ) // poll GPIFTRIG.7 Done bit
    {
        ;
    }

    // using register(s) in XDATA space, GPIF read byte from PERIPHERAL here
    *gdata = XGPIFSGLDATLNOX;
}

```

FIG. 8